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Attorney's Docket No.: 12816-093001 / S-2512

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al. Art Unit : 2811
Serial No. : 10/630,632 Examiner : Joseph H Nguyen
Filed : July 29, 2003
Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING
SEMICONDUCTOR CIRCUIT MODULES

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed October 15, 2004, applicant elects the invention of Group II drawn to the embodiment of claims 25-36. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 11-12-04

Faustino A. Lichauco
Reg. No. 41,942

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit November 12, 2004

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